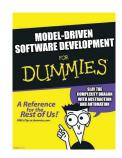
# Beyond Code: An Introduction to Model-Driven Software Development (CISC836)

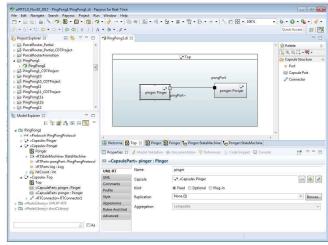


**UML-RT and IBM RSARTE: Part I** 

Juergen Dingel Sept 2021

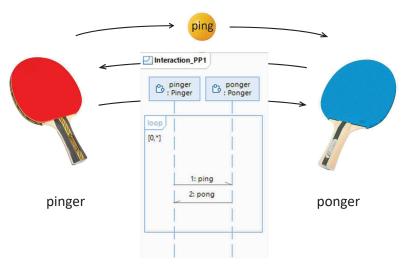
UML-RT CISC 836, Fall 2021 1

**UML-RT and RSARTE: Sneak Peek** 

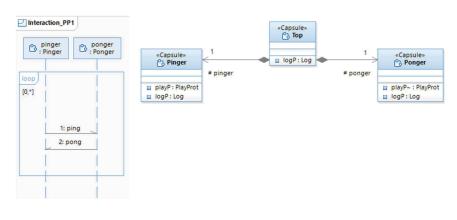


UML-RT CISC 836, Fall 2021 2

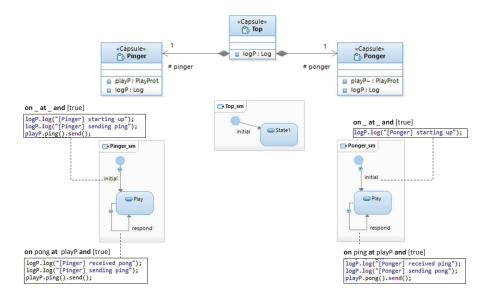
# **Example 1: Forever PingPong (1)**



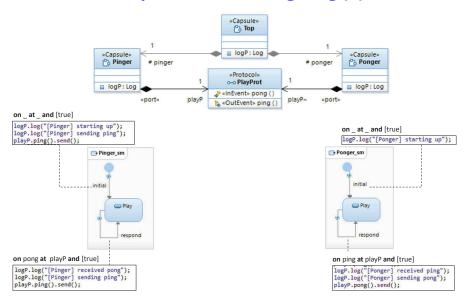
# **Example 1: Forever PingPong (2)**



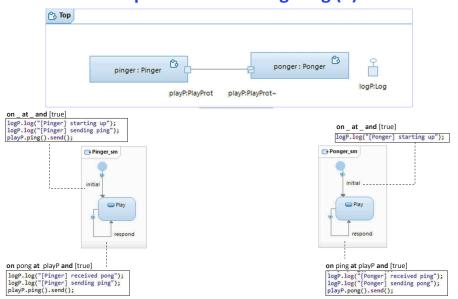
# **Example 1: Forever PingPong (3)**

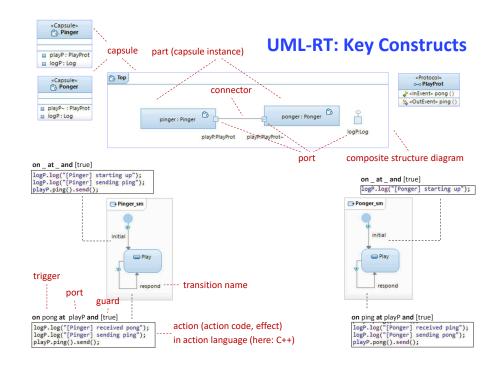


# **Example 1: Forever PingPong (4)**



# **Example 1: Forever PingPong (5)**





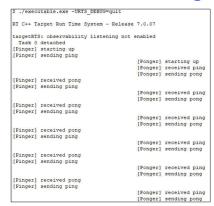
### **UML-RT: Tools**

- Commercial
  - IBM RSARTE
  - HCL Rtist
  - Protos eTrice
- Open source
  - Eclipse Papyrus-RT
- Web interface
  - Research prototype (language servers, containerization, etc)

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# **RSARTE Demo: Forever PingPong**



- Showing: editing, building, executing
- Variations
  - · In Pinger: No initial 'ping' message
  - · In Ponger: 'pong' sent 1 second after receipt of 'ping'

# **Modeling** Languages

### Modelica

- · Physical systems
- · Equation-based

### Simulink

- · Continuous control, DSP
- · time-triggered dataflow

### Stateflow

- Reactive systems
- Discrete control
- · State-machine-based

### **AADL** Lustre/SCADE

- · Embedded, real-time
- UML

### **UML MARTE**

• Embedded, real-time

### · Embedded real-time

- · Synchronous dataflow
- UML-RT
- Embedded, real-time State-machine-based



[Kelly, Tolvanen 2008]



[Voelter 2013]

# increasing generality UML-RT

increasing domain-specifity

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### **Examples in**

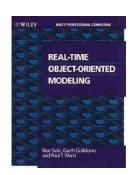




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# **UML-RT:** History

- Real-time OO Modeling (ROOM)
  - ObjecTime, early 1990 ties
- Major influence on UML 2
  - E.g., StructuredClassifier
- "RT subset of UML"
- Tools
  - ObjecTime Developer
  - IBM Rational RoseRT
  - IBM RSA-RTE
  - Eclipse Papyrus-RT
  - Protos eTrice



[Selic, Gullekson, Ward. Real-Time Object-Oriented Modeling. Wiley. 1994]

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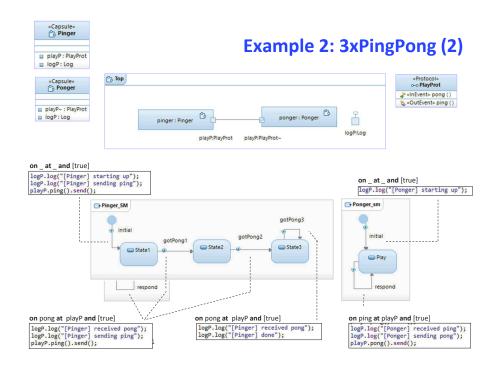
### **UML-RT: Characteristics I**

### Domain-specific inputs • Embedded systems w/ soft real-time constraints **Real-time System** Graphical, but textual syntax exists actors Small, cohesive set of concepts state Strong encapsulation outputs = · Actors (active objects) f(state,inputs) Explicit interfaces · Message-based communication in1 in2 inputs Resources managed by runtime system in1/out1 (RTS) • Message passing, logging, timers, capsule in2/out2 instantiation, ... out2 out1 out2

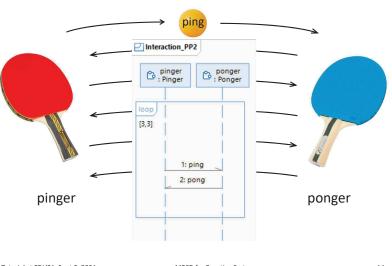
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IIMI-RT

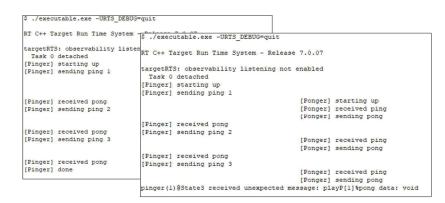


# **Example 2: 3xPingPong (1)**



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# **Example 2: 3xPingPong (3)**



### Variations

Remove trigger in 'gotPong3' transition in 'pinger'

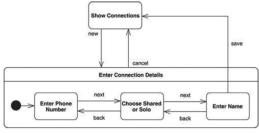
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### **UML-RT: Characteristics 2**

### Event-driven execution

- Every execution step by capsule C is caused by a message delivered to C (including, e.g., timeout messages)
- Challenge: when message m is delivered to state machine of C,
   C is able to handle m
  - ° group transitions (out of composite states)
  - ° defer/recall



M. Fowler. UML Distilled. 3rd Ed. 2004.

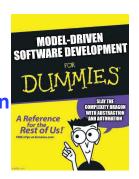
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# **Beyond Code:**

# An Introduction to Model-Driven Software Development (CISC836)



**UML-RT and RSARTE: Part II** 

Juergen Dingel Fall 2021

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# **UML-RT w/ RSARTE: Part II**

- Core concepts
  - Structural modeling
  - Behavioural modeling

# **UML-RT: Core Concepts (1)**

- Types
  - Capsules (active classes)
    - ° Capsule instances (parts)
  - Passive classes (data classes)
    - ° Objects
  - Protocols
  - Enumerations
- Structure
  - Attributes
  - Ports
  - Connectors

- Behaviour
  - Messages (events)
  - State machines
- Grouping
  - Package
- Relationship
  - Generalization
  - Associations

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# **UML-RT:**

# **Core Concepts (2)**

- Model
  - Collection of capsule definitions
  - 'Top' capsule containing collection of capsule instances (parts)
- Capsules
  - May contain
    - ° Attributes, ports, or other capsule instances (parts)
  - Behaviour defined by state machine
- Ports
  - Typed over protocol defining input and output messages
- State machine
  - Transition triggered by incoming messages
  - Action code can contain send statements that send messages over certain ports

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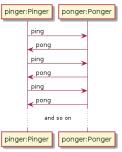


**□** Top

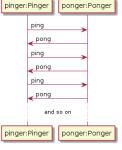
🖳 Model Explorer 🟻 📔 🏗 🏙 🦺

■ RootElement

pinger: Pinger



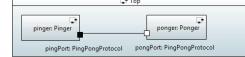
### «Protocol» PingPongProtocol out ping () € in pong ()



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# Capsules (1)

- Kind of active class
  - Attributes, operations
  - · Own, independent flow of control (logical thread)



- May also contain
  - Ports over which messages can be sent and received
  - Parts (instances of other capsules) and connectors
- Creation, use of instances tightly controlled
  - · Created by runtime system (RTS)
  - Cannot be passed around
  - Stored in attribute of another capsule (part)
  - Information flow only via messages sent to ports
  - $\Rightarrow$  better concurrency control and encapsulation
- Behaviour defined by state machine



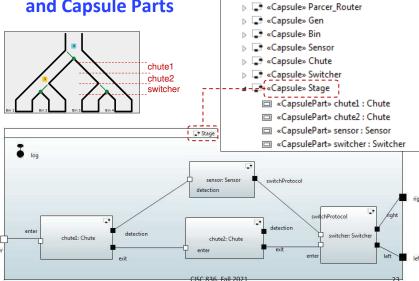




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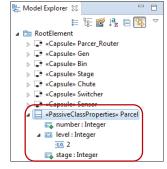
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# **Example: Capsules** and Capsule Parts



# **Passive Classes/Data Classes**

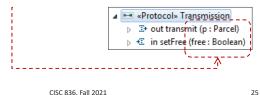
- Similar to regular classes
- Do not have independent flow of control
- Behaviour defined through operations
- Used to define data structures and operations on them



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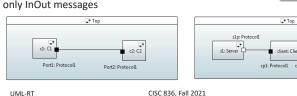
### **Protocols**

- Provide types for ports
- Define
  - Input messages
    - Services provided by capsule owning port
  - Output messages
    - ° Services required by capsule owning port
  - Input/output messages
- Messages can carry data



UML-RT

- Ports must be compatible
  - Both are instances of same protocol
  - - one is 'base' (i.e., not 'conjugated')
      - typically owned by 'client'
    - ° and the other is 'conjugated'
      - typically owned by 'server'
  - Or (symmetric)





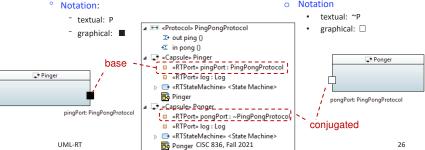




o Direction of messages declared in protocol is reversed

**Ports** 

### Notation



# External behaviour

"Boundary objects" owned by capsule

portName.msg(arg).send()

as declared in protocol

Typed over a protocol P

· base (not conjugated)

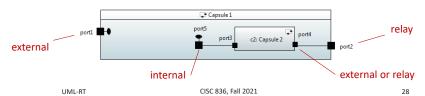
■ Have 'send' operation

Can be

- Provides (part of) externally visible functionality (isService=true)
- Incoming messages passed on to state machine (isBehaviour=true)

Ports: External, Internal, Relay

- Must be connected (isWired=true)
- Internal behaviour
  - As above, but not externally visible (isService=false)
  - Connect state machine with a capsule part
- Relay
  - Pass external messages to and from capsule parts

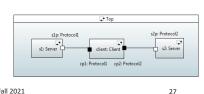


# **Connectors**

# Connect two ports

- Either (asymmetric)

  - ° only InOut messages



**□** Тор

pinger:Pinger

ping

pong

pong ping

pong

pinger:Pinger

and so on

pingPort: PingPongProtocol

«Protocol» PingPongProtoco

⊕ out ping ()

🐮 in pong ()

pongPort: PingPongProtocol

ponger:Ponger

ponger:Ponger

# **Ports: System**

- Connects capsule to Runtime System (RTS) library via corresponding system protocol
- Provides access to RTS services such as

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Timing: setting timers, time out message

timer2Port.informIn(RTTimespec(10, 2));

// set timer that will expire in 10 secs and 2 nanosecs

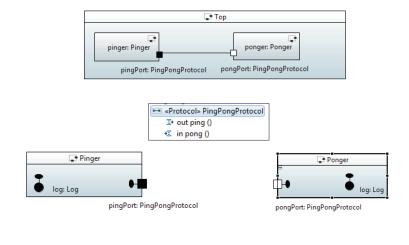
When timer expires, 'timeout' message will be sent over timer2Port

Log: sending text to console

logPort.log("Ready to self-destruct")

Frame: incarnate, destroy capsule instances

**Example: PingPong** 

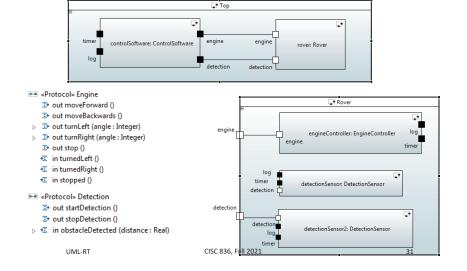


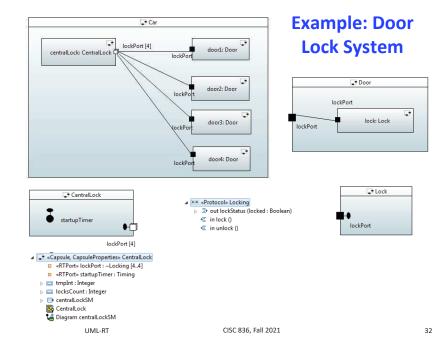
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UML-RT

# **Example: Rover**

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# **UML-RT w/ RSARTE: Part II**

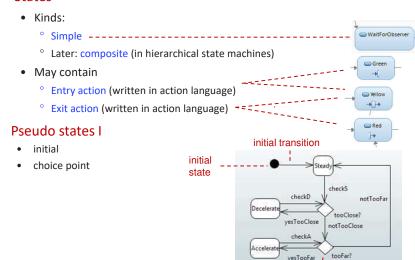
- Core concepts
  - Structural modeling
  - Behavioural modeling

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# **States I: Simple and Pseudo**

### States

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choice point

### **State Machines**

### **States**

- Capture relevant aspects of history of object
- Determine how object can respond to incoming messages
- May have invariants associated with them

### **Pseudo states**

- Don't belong to description of lifetime of object
  - $\Rightarrow$  object cannot be 'in' a pseudo state
- Helper constructs to define complex state changes

### **Transitions**

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• Describe how object can move from one state to next in response to message input

VendingMachine

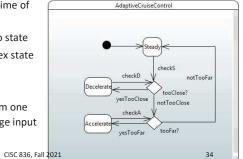
Ready

insertDollar1

dot1Dollar

toblerone

Got2Dollar2



**Transitions** 

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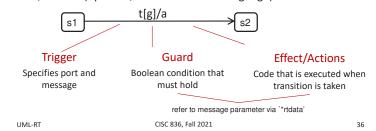
### Kinds:

### Basic

• Later: group (in hierarchical state machines)

### Consists of

- Triggers
  - ° Transitions out of pseudo states (initial, choice) don't have triggers
  - ° Transitions out of non-pseudo state should have at least one trigger
- Guards (optional, written in action language)
  - ° Transitions out of initial state should not have guards
- Effect/Actions (optional, written in action language)



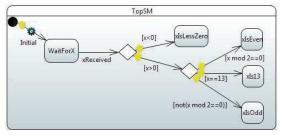
# Transitions Into and Out of Pseudo States

### Initial

- · Incoming transition: impossible
- Outgoing transition: no guard, no trigger, but can have action code

### Choice point

- Incoming transitions: can have guard, triggers, action code
- Outgoing transitions:
  - ° No trigger, but should have guard
  - Guards should be pairwise disjoint (i.e., non-overlapping)
  - Collection of guards should be exhaustive

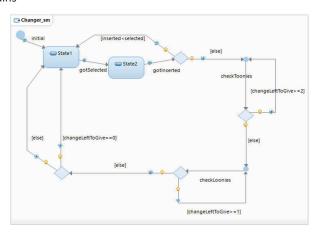


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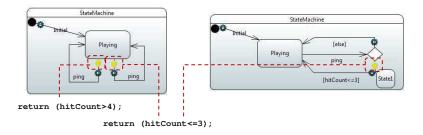
# **UML-RT: Characteristics 3**

### State machines

- pseudo states (e.g., choice, junction, history)
   => transition chains
- Transition chains



# **Guards on Transitions out of Basic States**



# Better to use choice points

· Make branching in control flow more explicit

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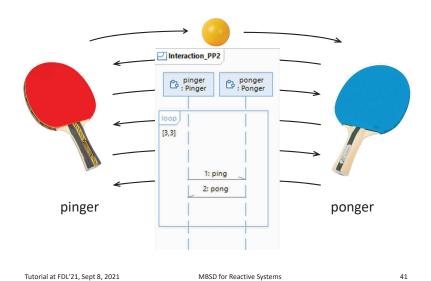
# **Action Language**

- Language used in
  - guards to express Boolean expressions
  - entry action, exit action, transition effects to read and update attribute values, send messages
- Typically: C/C++, Java
- ⇒ State machines are a hybrid notation combining
  - $^{\circ}\,$  graphical notation for state machines and
  - ° textual notation for source code in actions
- ⇒ UML and UML-RT State Machines
  - ° different from, e.g., Finite Automata
  - ° closer to 'extended hierarchical communicating state machines' [6]

[6] R. Alur. Formal Analysis of Hierarchical State Machines. Verification: Theory and Practice. 2003.

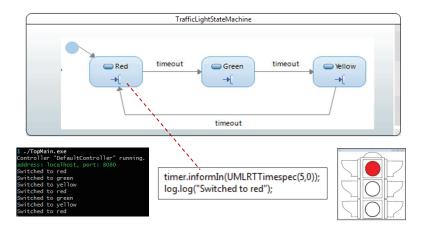
UML-RT CISC 836, Fall 2021 40

# **Example 3: 3xPingPong + Choice (1)**



### «Capsule» **Example 3: 3xPingPong + Choice (2)** count: int playP : PlayProt ■ logP: Log «Protocol» o-o PlayProt «Capsule» 🕞 «OutEvent» ping () ■ playP~ : PlayProt ■ logP: Log ponger: Ponger pinger : Pinger logP:Log playP:PlayProt playP:PlayProt~ on \_ at \_ and [true] logP.log("[Pinger] starting up"); logP.log("[Pinger] sending ping"); playP.ping().send(); on at and [true] logP.log("[Ponger] starting up"); Ponger\_sm Pinger\_SM initial [count<3] Play initial on pong at playP and [true] on pong at playP and [true] on ping at playP and [true] logP.log("[Pinger] received pong"); logP.log("[Pinger] sending ping"); playP.ping().send(); logP.log("[Pinger] received pong"); logP.log("[Pinger] done"); logP.log("[Ponger] received ping"); logP.log("[Ponger] sending pong"); playP.pong().send();

# **Example: Action Code, Timers, Logging**



// SM is in stable state, i.e., ready to process messages

- 1. Message m has arrived and is delivered to SM
- 2. If SM has no transition that is enabled, m is 'dropped'
- 3. If transition t enabled in SM. i.e..
  - source state of t is active,
  - trigger of t matches m, and
  - guard (if any) of t evaluates to 'true'

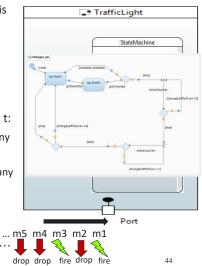
then execute transition chain to starting at t:

- 1. execute exit action of source state of t, if any
- 2. execute action code along tc, if any
- 3. execute entry code of target state of tc, if any

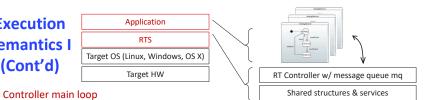
// SM is in stable state

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# **Execution Semantics I**



# **Execution** Semantics I (Cont'd)

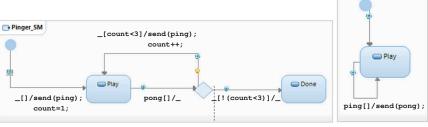


```
WHILE (1) {
  <m, sm> = dequeue (mq);
 IF can find transition t in sm such that enabled (m,t,currRTState) THEN
     currRTState = execChain(t,currRTState);
     report 'Unexpected message m';
WHERE
  currRTState is ...?
```

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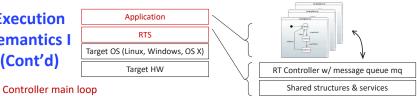
Ponger\_sm

# **Example: Execution Semantics I**



actStates	vars	mq	
	count=_	[]	pinger
Play,Play	count=1	[ping]	ponger
		[pong]	pinger
	count=2	[ping]	Pulber
		[pong]	
	count=3	[ping]	
		[pong]	
Done, Play Tutorial at FDL'21, Sept 8, 2021		[ ] MBSD for Reactive Systems	47

# **Execution** Semantics I (Cont'd)



```
<actStates, vars, mq> := execInits();
                                                                         How to add support
WHILE (1) {
                                                                        for entry/exit actions?
 <m, sm> = dequeue(MQ);
 IF can find transition t in sm such that enabled(m,t,<actStates,vars,mq>) THEN
     <actStates, vars, mq> = execChain(t, <actStates, vars, mq>);
     report 'Unexpected message m';
WHERE
enabled(m,t,<actStates,vars,mq>) =
              source(t) in actStates, trigger(t) matches m, and eval(guard(t),vars)='true'
execChain(t, <actStates, vars, mq>) =
              <actStates, vars, mq> := exec(effect(t), <actStates, vars, mq>);
              WHILE target(t) is choice point {
                  find t' such that source(t')=target(t) and eval(guard(t'),vars)='true';
                  <actStates, vars, mq> := exec(effect(t'), <actStates, vars, mq>);
              RETURN <actStates, vars, mq>;
```

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# **Example: Execution Semantics I (Cont'd)**

```
$ ./executable.exe -URTS DEBUG=quit
RT C++ Target Run Time System - Release 7.0.07
targetRTS: observability listening not enabled
 Task 0 detached
[Pinger] starting up
[Pinger] sending ping 1
                                          [Ponger] starting up
                                         [Ponger] received ping
[Ponger] sending pong
[Pinger] received pong
[Pinger] sending ping 2
                                          [Ponger] received ping
                                          [Ponger] sending pong
[Pinger] received pong
[Pinger] sending ping 3
                                          [Ponger] received ping
                                          [Ponger] sending pong
[Pinger] received pong
[Pinger] done
```

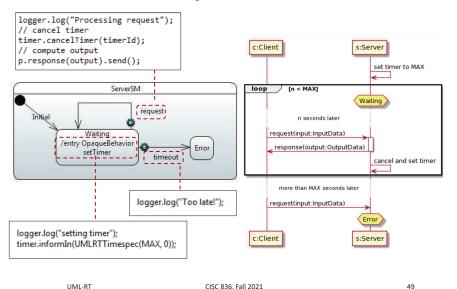
# Things to try

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• In its initial transition, pinger sends 2 'ping' messages

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# **Example: Timers**

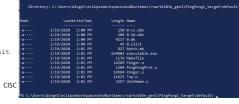


# RSARTE (Cont'd)

- Use
  - (model, generate, build, run)^\*
- Generated code
  - <workspace>/<projectName>\_target/
- RTS
  - <RSARTE installation dir>/rsa\_rt/C++/TargetRTS/
- Building generated code
  - executable:
    - ° <workspace>/<projectName>\_target/default
- Running generated code
  - from inside RSARTE
  - · from command line

IIMI-RT

>> ./executable.exe -URTS\_DEBUG=quit



### **RSARTE**

### Download and installation

- Queen's version:
  - ° https://jahed.ca/rsarte
- Java 8, 64 bits
- Q&A forums
  - CISC 836 pages on OnQ at http://ong.queensu.ca/

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# RSARTE (Cont'd)

### Tips and tricks

- Common mistakes
  - ° Forgot: 'send' statement or trigger

```
logP.log("[Pinger] sending first ping");
pingP.ping();
```

° Execution results in a 'stackdump'? C++ issue in action code, e.g.,

```
int delay = 500000000;
logP.log("[%s] twiddling for %d nanoseconds", delay, "Pinger");
// wait for 0.5 seconds; 10^9 nsec = 1 sec
timingP.informIn(RTTimespec(0, delay));
```

- ° When using 'Code View':
  - don't confuse tabs:
  - · for transitions: 'effect' vs 'guard'
  - · for states: 'entry' vs 'exit'
  - ensure changes saved properly

Examples UML-RT

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# Beyond Code: An Introduction to Model-Driven Software Development (CISC836)

MODEL-DRIVEN
SOFTWARE DEVELOPMENT

DUMMIES

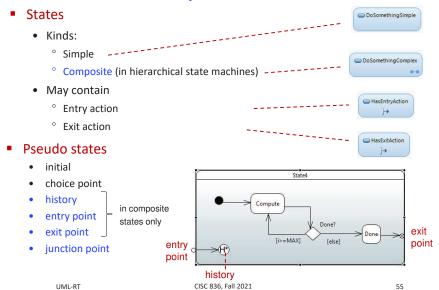
OMNETOT MAGNOTUM
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AND ATTOMOTON

**UML-RT and RSARTE: Part III** 

Juergen Dingel Fall 2021

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# **States II: Composite and Pseudo**



# **UML-RT/RSARTE:** Part III

### More on

- State machines
  - ° States
    - Simple
    - Composite
  - ° Pseudo states
    - Initial
    - Choice point
    - Entry point
    - Exit point
    - History
    - Junction
- Execution semantics
  - ° Run-to-completion

### Design guidelines

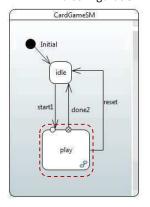
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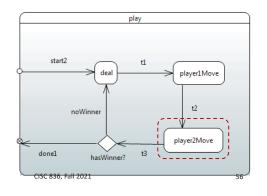
### Source state is composite

# **Group Transitions**

# Example:

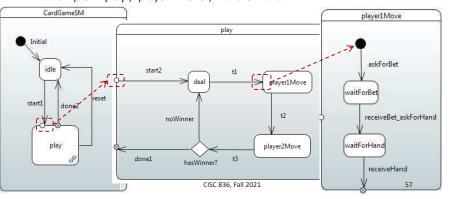
- Start configuration <'play','player2Move'>
- Execute transition 'reset':
  - ° exit code 'player2Move', exit code 'play', effect 'reset', entry code 'idle'
- End configuration <'idle'>





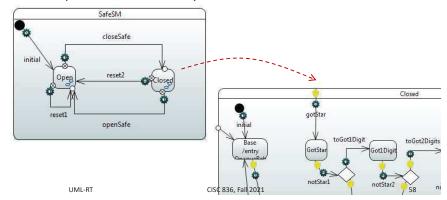
# **State Configuration**

- States can be active: flow of control resides at state
- If a substate is active, its containing superstate is, too
- Active state really is a tuple: list of active states
- Stable state configuration: no pseudo states and ends in basic state
- Example: <'play', 'player1Move', 'waitForHand'>



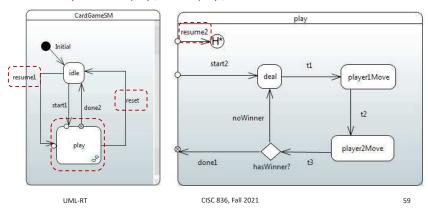
# **Entry and Exit Points**

- Required boundary pseudo states for transitions crossing boundaries of composite states
- Transition ending at entry point w/o outgoing transitions: implicit return to history



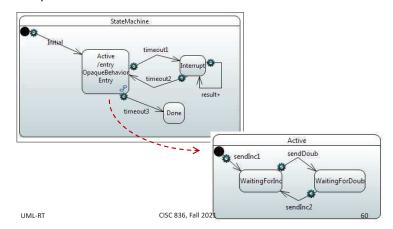
# **History**

- Re-establish full state configuration that was active when containing state was active most recently
- If entering state for first time, go to initial state
- Example: from <'play', s> to <'play', s> with 'reset' 'resume1'



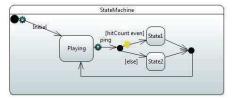
# History (Cont'd)

- History pseudo state does not need to be given explicitly
- Transition ends at boundary of composite state: Implicit return to history



### **Junction Points**

 Can be used to split and merge control flow



### Warning:

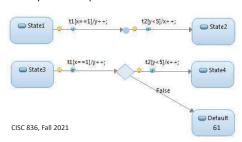
- Static evaluation: All guards on transitions connected by junction points evaluated BEFORE first transition is taken
- Transitions taken only when fully enabled path exists

Choice points

Dynamic evaluation:
 Guards evaluated as
 transitions are executed

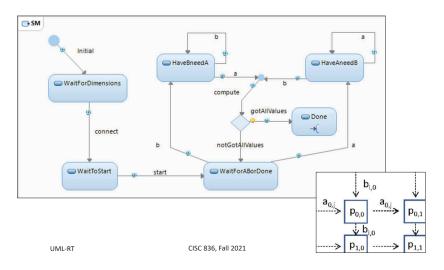
Pros/cons?

UML-RT



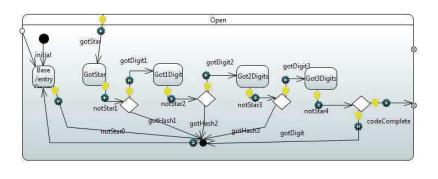
# **Junction Points (Cont'd)**

• State machine of processor in matrix multiplication:



# **Junction Points (Cont'd)**

Merge useful to avoid duplication of action code



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# **Run-to-Completion**

- The event processing of state machines follows 'run-tocompletion' semantics
- Dispatching of message triggers execution of possibly entire chain of transitions ('exec' on previous slide)
- Execution lasts until stable state configuration has been reached (last state in transition chain not a pseudo state)
- During transition execution, no other message will be dispatched
- $\Rightarrow$  execution triggered by message treated as one unit
- $\Rightarrow$  no 'interleaved' processing of messages
- $\Rightarrow$  less potential for bugs

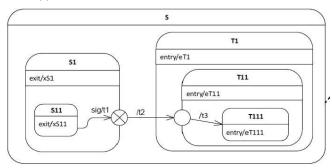
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# **Example**

### Assume

Controller main loop

- State configuration: <S,S1,S11>
- Message 'sig' dispatched to state machine
- What happens?

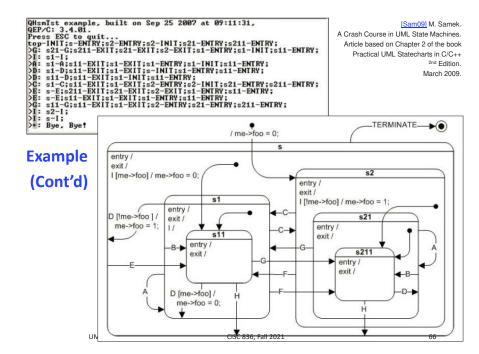


[UML2.5.1] UML Specification v2.5.1. Dec 2017. Page 381 https://www.omg.org/spec/UML/2.5.1/PDF

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# **Execution Semantics II**

```
WHILE (1) {
 m = dequeue(MQ);
 IF can find transition t such that enabled(rts,m,t) THEN rts = exec(rts,t);
 ELSE report 'Unexpected message m';
WHERE
enabled(<ssc, vars>, m, t) = (1) source(t) is active in ssc, (2) trigger(t) matches m,
                     (3) eval(guard(t), vars)='true', and
                     (4) source(t) does not contain any other state satisfying (1),(2),(3)
exec(<ssc,vars>,t) =
                   LET ssc=<s<sub>1</sub>, ..., s<sub>i-1</sub>, s<sub>i</sub>, s<sub>i+1</sub>, ..., s<sub>n</sub>> where s<sub>i</sub>=source(t) IN
                   FOR j=n to i+1 {execute exit of s4};
                   <targetOfChain, vars> = execChain(t, vars);
                   s<sub>k</sub> = leastCommonAncestor(source(t), targetOfChain);
                  LET \langle s_{n}, s'_{n}, ..., s'_{n} \rangle be containment hierarchy where s'_{n}=targetOfChain IN
                      RETURN <<s_1, ..., s_{k-1}, s_k, s'_1, ..., s'_m>, vars>
execChain(t,vars) =
                execute exit of source(t), if any;
                execute effect of t, if any;
                execute entry of target(t), if any;
                WHILE target(t) is pseudo state {
                    find t' such that source(t')=target(t) and eval(guard(t'))='true';
                    execute exit of state(source(t')), if any;
                    execute effect of t', if any;
                    execute entry of state(target(t')), if any;
                                                                    UML2.5.1 Spec, Section 14.2.3
                RETURN target(t); }
                                                         http://www.omg.org/spec/UML/2.5.1/PDF 67
```



# **UML-RT: Design Guidelines**

### General

- Names
  - ° Descriptive, correct (syntactically and semantically), consistent
- Readable, clear layout of models
- Remove/cancel what is not needed anymore (timers, capsule parts)
- Avoid duplication (through, e.g., operations, junction points for merging, entry and exit code)

### Capsules

- Low coupling, high cohesion (look at connectors, message traffic, protocols)
- · Avoid overly deeply nested capsule definitions

### State machines

- · Avoid unreachable states and transitions
- Avoid overly deeply nested composite states
- Avoid composite states with only one substate

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# **UML-RT: Design Guidelines (Cont'd)**

### Action code

- Short, simple, terminating, readable, reachable (i.e., not dead)
- Avoid 'hidden' states (e.g., flags and complex control flow)

### Junction points

· Only use for merging

### Transitions

- Guards: short, simple, readable, side-effect-free
- Out of choice points: at least two, guards exhaustive and exclusive, no trigger
- Out of initial, entry, exit, junction: no guard, no trigger
- Out of non-pseudo state: no guards
- Use different kinds (external, local, internal) appropriately
- Avoid dropped, 'unexpected' messages
- Make copy of complex message parameters upon receipt
- Can't cross 'state boundaries' w/o going through an entry or exit point

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# **UML-RT: Design Guidelines (Cont'd)**

- Correct use of constructs and services offered by UML-RT, RTS or C++
  - Random number generator
    - Initialize once at startup (e.g., using srand(time(0)))
  - Replication

### Observability

 Insert informative log statements at suitable places to facilitate reasoning about the model (debugging, error localization)

### Format:

 $\label{logger.log(name of capsule part] (Name of state)...(Name of substate) info")} Where 'info' describes$ 

- ° message and/or data received, or
- ° attribute values
- · Consider use of command-line parameters to facilitate testing

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