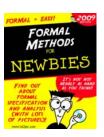
CISC422/853: Formal Methods in Software Engineering: Computer-Aided Verification



Topic 5: Model Checking, Part 1

Juergen Dingel Feb, 2009

Readings: Spin book

- Chapter 11 (Using Spin)
- Chapter 8, pages 167-178 (Search Algorithms)

■ The SumToN Example (Source: CIS842 @ KSU)

Outline

- Use this simple example to explain
 - · schedules
 - · computation trees
 - 3 forms of exploration:
 - ° random
 - ° interactive
 - exhaustive

CISC422/853, Winter 2009

Model Checking, Part 1

2

SumToN

```
system SumToN {
const PARAM { N = 1 };
typealias byte int wrap (0,255);
byte x := 1;
byte t1;
byte t2;
 active thread Thread1() {
  loc loc0:
   when x != (byte)0 do \{ t1 := x; \}
   goto loc1;
  loc loc1:
   do \{t2 := x; \}
   goto loc2;
  loc loc2:
   do \{ x := t1 + t2; \}
   goto loc0;
```

```
active thread Thread2() {
    loc loc0:
        when x != (byte)0 do { t1 := x; }
        goto loc1;

    loc loc1:
        do { t2 := x; }
        goto loc2:
        do { x := t1 + t2; }
        goto loc0;
}

active thread Thread0() {
    loc loc0:
        do { assert (x != (byte)PARAM.N); }
        return;
}
```

SumToN (Cont'd)

```
system SumToN {
                                               active thread Thread2() {
const PARAM { N = 1 };
                                                 loc loc0:
typealias byte int wrap (0,255);
                                                  when x != (byte)0 do \{ t1 := x; \}
                                                  goto loc1;
byte x := 1;
byte t1;
                                     declare a 'byte' to be an
byte t2;
                                     integer with range 0..255
active thread Thread1() {
                                     that will 'wrap around' when
 loc loc0:
                                     operated on
  when x != (byte)0 do \{ t1 := x; \}
  goto loc1;
  loc loc1:
  do { t2 := x; }
                                               active thread ThreadO() {
  goto loc2;
                                                  do { assert (x != (byte)PARAM.N); }
                                                  return;
  do \{ x := t1 + t2; \}
  goto loc0;
```

CISC422/853, Winter 2009 Model Checking, Part 1 Source: 842@KSU CISC422/853, Winter 2009

Model Checking, Part 1

SumToN (Cont'd)

```
system SumToN {
                                                 active thread Thread2() {
const PARAM { N = 1 };
                                                  loc loc0:
typealias byte int wrap (0,255);
                                                   when x != (byte)0 do \{ t1 := x; \}
                                                   goto loc1;
byte x := 1;
byte t1;
                                      declare three byte-
byte t2;
                                      sized variables
 active thread Thread1() {
 loc loc0:
                                                  loc loc2:
  when x != (byte)0 do \{ t1 := x; \}
                                                   do \{ x := t1 + t2; \}
  goto loc1;
                                                   goto loc0;
 loc loc1:
  do { t2 := x; }
                                                 active thread ThreadO() {
  goto loc2;
                                                   do { assert (x != (byte)PARAM.N); }
 loc loc2:
                                                   return:
   do \{ x := t1 + t2; \}
   goto loc0;
```

CISC422/853, Winter 2009

Model Checking, Part 1

Model Checking, Part 1

Source: 842@KSU

SumToN (Cont'd)

```
system SumToN {
                                                active thread Thread2() {
const PARAM \{ N = 1 \};
                                                 loc loc0:
typealias byte int wrap (0,255);
                                                   when x != (byte)0 do \{ t1 := x; \}
                                                   goto loc1;
byte x := 1;
byte t1;
                                                 loc loc1:
byte t2;
                                                   do \{t2 := x; \}
                                                   goto loc2;
active thread Thread1() {
 loc loc0:
                                                 loc loc2:
  when x != (byte)0 do { t1 :≥ x; }
                                                   do \{ x := t1 + t2; \}
   goto loc1;
                                                   goto loc0;
  loc loc1:
   do \{t2 := x; \}
                                                                 read0() {
                                     Each thread reads
  goto loc2;
                                                                 != (byte)PARAM.N); }
                                     the value of x in t1.
  loc loc2:
                                     then t2, then sums
   do \{ x := t1 + t2; \}
   goto loc0;
                                     t1 and t2 to get a
                                     new value for x
                                                      Source: 842@KSU
```

SumToN (Cont'd)

```
system SumToN {
                                                 active thread Thread2() {
const PARAM { N = 1 };
                                                  loc loc0:
typealias byte int wrap (0,255);
                                                   when x != (byte)0 do \{ t1 := x; \}
                                                        loc1;
                         The "main" thread
byte x := 1;
byte t1;
                         asserts that x is not
 byte t2;
                                                        2 := x; }
                         equal to the value of N
 active thread Thread1() {
   when x != (byte)0 do \{ t1 := x; \}
                                                   do \{ x := t1 + t2; \}
   goto loc1;
                                                   goto loc0;
  loc loc1:
   do \{t2 := x; \}
                                                 active thread ThreadO() {
   goto loc2;
                                                   do { assert (x != (byte)PARAM.N); }
  loc loc2:
                                                   return;
   do \{ x := t1 + t2; \}
   goto loc0;
```

SumToN

Model Checking, Part 1

```
system SumToN {
                                               active thread Thread2() {
const PARAM { N = 1 };
                                                loc loc0:
typealias byte int wrap (0,255);
                                                 when x != (byte)0 do \{ t1 := x; \}
                                                 goto loc1;
byte x := 1;
byte t1;
                                                loc loc1:
byte t2;
                                                 do \{t2 := x; \}
             · This transition can be
                                                 goto loc2;
active thread arbitrarily interleaved
 loc loc0:
                                                 loc loc2:
             with all others from
  when x !=
                                                 do \{ x := t1 + t2; \}
  goto loc1; Thread1 and Thread2.
                                                 goto loc0;

    This is how we check

             invariants (properties
  do { t2 :=
                                               active thread ThreadO() {
  goto loc2; that should always
                                                 do { assert (x != (byte)PARAM.N); }
             hold)
  loc loc2:
                                                  return;
  do \{ x := t1 + t2; \}
  goto loc0;
```

Source: 842@KSU 7 CISC422/953 W/i

CISC422/853, Winter 2009

CISC422/853, Winter 2009

Model Checking, Part 1

10⁶ \$ Question

Can the assertion in the SumToN example be violated? If so, how?

- Answering this question requires us to reason about possible schedules (i.e., orderings of instruction execution)
- Let's try to find schedules that cause the assertion to be violated for various values of N...

CISC422/853. Winter 2009

Model Checking, Part 1

9

SumToN Assertion Violation

```
active thread Threadk() {
      loc loc0:
       when x != (byte)0 do {
            t1 := x: 
       goto loc1;
k:1 loc loc1:
       do \{t2 := x;\}
       goto loc2;
k:2 loc loc2:
       do \{ x := t1 + t2; \}
       goto loc0;
     active thread ThreadO() {
     loc loc0:
       do { assert (x !=
             (byte)PARAM.N); }
       return;
```

CISC422/853, Winter 2009

Violating schedule for N = 1:

```
[0, 0, 0, x = 1, t1 = 0, t2 = 0]
\leftarrow 0:0 \rightarrow [-, 0, 0 x = 1, t1 = 0, t2 = 0]
                                 violation!
```

...that was easy!

Source: 842@KSU

SumToN Assertion Violation (Cont'd)

```
active thread Threadk() {
      loc loc0:
       when x != (byte)0 do {
            t1 := x; }
       goto loc1;
      loc loc1:
        do { t2 := x; }
       goto loc2;
k:2 loc loc2:
        do \{ x := t1 + t2; \}
       goto loc0;
     active thread ThreadO() {
0:0 loc loc0:
       do { assert (x !=
             (byte)PARAM.N); }
       return;
```

 1^{st} violating schedule for N = 2:

(initial state)
$$[0, 0, 0, x = 1, t1 = 0, t2 = 0]$$
 $\leftarrow 1:0 \rightarrow [0, 1, 0, x = 1, t1 = 1, t2 = 0]$
 $\leftarrow 1:1 \rightarrow [0, 2, 0, x = 1, t1 = 1, t2 = 1]$
 $\leftarrow 1:2 \rightarrow [0, 0, 0, x = 2, t1 = 1, t2 = 1]$
 $\leftarrow 0:0 \rightarrow [-, 0, 0, x = 2, t1 = 1, t2 = 1]$

violation!

SumToN Assertion Violation (Cont'd)

Model Checking, Part 1

```
active thread Threadk() {
      loc loc0:
       when x != (byte)0 do {
            t1 := x: 
       goto loc1;
      loc loc1:
       do { t2 := x; }
       goto loc2;
k:2 loc loc2:
       do \{ x := t1 + t2; \}
       goto loc0;
     active thread Thread0() {
0:0
     loc loc0:
       do { assert (x !=
             (byte)PARAM.N); }
       return;
```

 2^{nd} violating schedule for N = 2:

```
[0, 0, 0, x = 1, t1 = 0, t2 = 0]
\leftarrow 2:0 \rightarrow [0, 0, 1, x = 1, t1 = 1, t2 = 0]
\leftarrow 2:1 \rightarrow [0, 0, 2, x = 1, t1 = 1, t2 = 1]
\leftarrow 2:2 \rightarrow [0, 0, 0, x = 2, t1 = 1, t2 = 1]
\leftarrow 0:0 \rightarrow [-, 0, 0] x = 2, t1 = 1, t2 = 1]
                                    violation!
```

CISC422/853, Winter 2009 Model Checking, Part 1 Source: 842@KSU

CISC422/853, Winter 2009

Model Checking, Part 1

SumToN Assertion Violation (Cont'd)

```
active thread Threadk() {
    loc loc0:
    when x != (byte)0 do {
        t1 := x; }
    goto loc1;

k:1 loc loc1:
    do { t2 := x; }
    goto loc2;

k:2 loc loc2:
    do { x := t1 + t2; }
    goto loc0;
}

active thread Thread0() {
    loc loc0:
    do { assert (x != (byte)PARAM.N); }
    return;
}
```

3^{rd} violating schedule for N = 2:

(initial state)
$$[0, 0, 0, x = 1, t1 = 0, t2 = 0]$$
 $\begin{array}{l}
-1:0 \\
-2:0 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:2 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1 \\
-2:1$

CISC422/853, Winter 2009

Model Checking, Part 1

Source: 842@KSU

13

Computation Trees

We can think of the possible schedules (execution traces) as forming a computation tree

system state

computation tree

choice points
(multiple enabled transitions)

CISC422/853, Winter 2009

Model Checking, Part 1

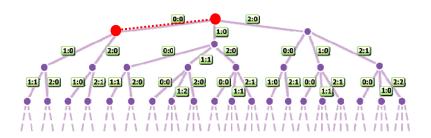
Source: 842@KSU

1.4

Computation Trees (Cont'd)

We can think of the possible schedules (execution traces) as forming a computation tree...

First example trace (schedule)

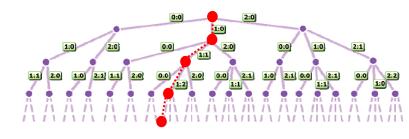


Computation Trees (Cont'd)

We can think of the possible schedules (execution traces) as forming a

computation tree...

Second example trace (schedule)



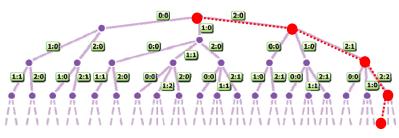
CISC422/853, Winter 2009 Model Checking, Part 1 Source: 842@KSU 15 CISC422/853, Winter 2009 Model Checking, Part 1 Source: 842@KSU

Computation Trees (Cont'd)

We can think of the possible schedules (execution traces) as forming a

Third example trace (schedule)

computation tree...



CISC422/853, Winter 2009

Model Checking, Part 1

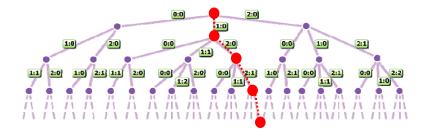
Source: 842@KSU

17

Computation Trees (Cont'd)

We can think of the possible schedules (execution traces) as forming a computation tree...

Fourth example trace (schedule)



CISC422/853, Winter 2009

Model Checking, Part 1

Source: 842@KSU

Computation Trees, Formally

Given a FSA A, the *computation tree* T_A of A is obtained by

- s_0 is root of T_A for $s_0 \in A.S_0$
- "unwinding" the tree using A.δ:
 - $^{\circ}~$ for every s in $T_{A},$ s' is successor of s iff (s, l, s') $\in A.\delta$ for some l

Example:

Observations:

- paths(T_A) = runs(A)
- a state may occur more than once along a path in TA
- states w/o outgoing transitions in A are leaves in T_A
- \bullet every path in T_{A} is infinite iff transition relation $A.\delta$ is total

Aside: Model Checking "On-the-Fly"

- Let
 - D be representation of a system in input language of some model checker MC
 - iFSA_D iFSA/computation tree corresponding to D
- Two kinds of model checkers:
 - On-the-fly: MC computes iFSA_D step-by-step during exploration
 - ° Examples: Spin, Bogor
 - Not on-the-fly: MC computes iFSA_D before it starts the exploration
 - ° Example: NuSMV
- What are the pros and cons of "on-the-fly" model checking?

CISC422/853, Winter 2009 Model Checking, Part 1 19 CISC422/853, Winter 2009 Model Checking, Part 1 20

Aside: Model Checking Symbolically

 Model checkers that are not on the fly, typically use a sophisticated data structure called

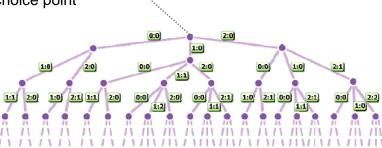
Binary Decision Diagrams (BDDs)

to represent iFSA_D

- BDDs represent iFSA_D "symbolically" in a graph rather than explicitly
- For many D, BDDs allow transition relation of iFSA_D to be represented very efficiently (through lots of sharing)
- SMV, Cadence SMV, and NuSMV:
 - BDDs were first used for model checking in SMV (Symbolic Model Verifier, developed at CMU)
 - Cadence SMV: developed at Cadence Labs (for Windows)
- NuSMV: open-source effort by IRST (Trento, Italy) and CMU
 CISC422/853, Winter 2009
 Model Checking, Part 1

Random Simulation

In a random simulation, Bogor randomly chooses a branch at a choice point



CISC422/853, Winter 2009

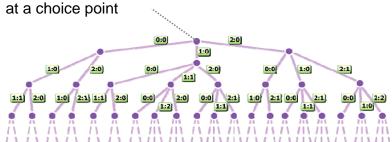
Model Checking, Part 1

Source: 842@KSU

22

Guided Simulation

In a guided simulation, Bogor asks the user which transition to take



Exhaustive Exploration

- Both in random and interactive exploration, only one path is explored at a time
- If during random and interactive exploration
 - violation found, then system incorrect (due to soundness)
 - no violation found, then ??
- Little better than a using a good debugger
- We really want exhaustive exploration:
 - Using exhaustive exploration, all executions (schedules) of the system are checked for violations. So, if
 - ° violation found, then system incorrect (soundness)
 - ° no violation found, then system correct (completeness)

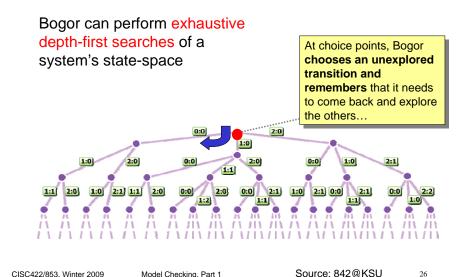
CISC422/853, Winter 2009 Model Checking, Part 1 Source: 842@KSU 23 CISC422/853, Winter 2009 Model Checking, Part 1 24

Exhaustive Exploration (Cont'd)

- Model checkers allow you to perform exhaustive explorations
- Challenge: Exploration may take
 - a long time, because
 - the system has lots of reachable states
 - the system has lots of executions
 - ° a lot of memory, because
 - states contain lots of information (e.g., processes have lots of variables, or variables range over complex data structures)
- Need safe optimizations (Topic 8)
- But before that, we discuss algorithms for exhaustive exploration. All are based on DFS and BFS
- Using Bogor as example (Spin works similarly)

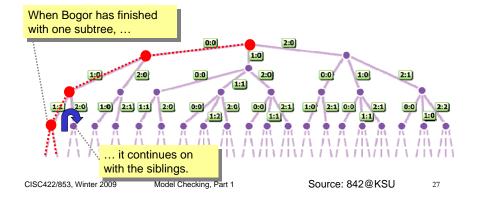
CISC422/853, Winter 2009 Model Checking, Part 1 25

Exhaustive Depth-first Search



Exhaustive Depth-first Search (Cont'd)

Bogor can perform exhaustive depth-first searches of a system's state-space

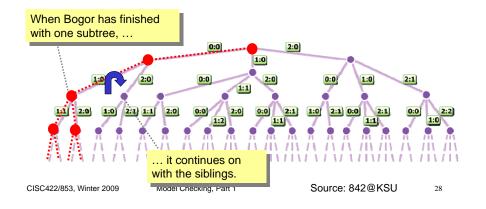


Exhaustive Depth-first Search (Cont'd)

Model Checking, Part 1

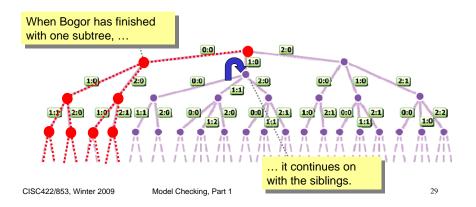
Bogor can perform exhaustive depth-first searches of a system's state-space

CISC422/853, Winter 2009



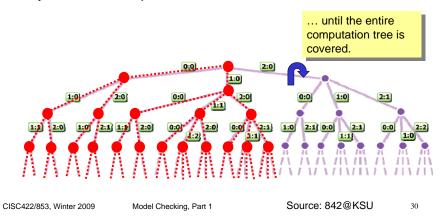
Exhaustive Depth-first Search (Cont'd)

Bogor can perform exhaustive depth-first searches of a system's state-space



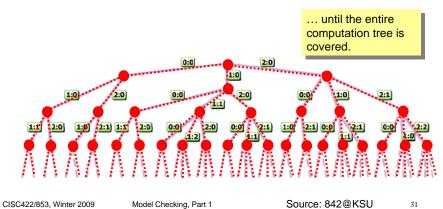
Exhaustive Depth-first Search (Cont'd)

Bogor can perform exhaustive depth-first searches of a system's state-space

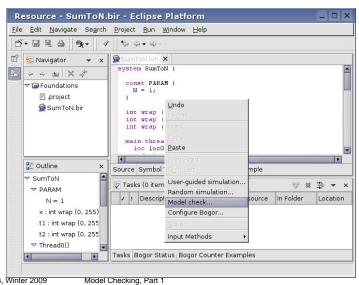


Exhaustive Depth-first Search (Cont'd)

Bogor can perform exhaustive depth-first searches of a system's state-space.

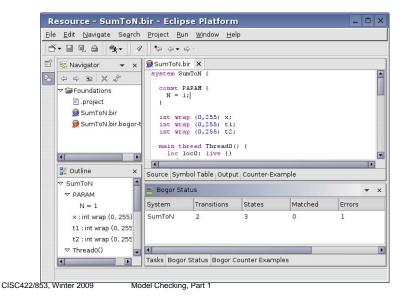


DFS with Bogor

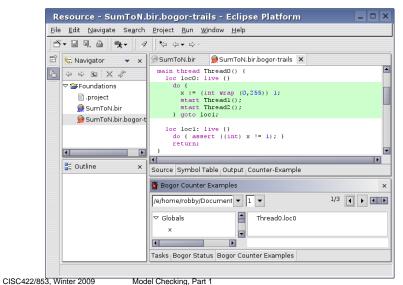


CISC422/853, Winter 2009

Bogor Output



Bogor Counterexample Display



DFS Basic Data Structures

State vector

· contains values of all variables and program counters for each process

Depth-first stack

 contains states (or transitions) encountered down a certain path in computation tree

Seen state set

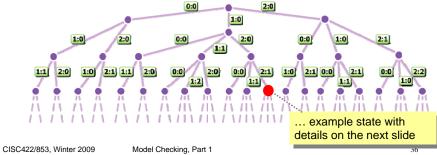
· contains state vectors for all states that have been checked already (seen) during depth-first search

Note

- values of these data structures shown in abstract manner only
- · actual implementation of most model-checkers uses multiple clever representations to obtain a highly optimized search algorithm

SumToN State Vector Example

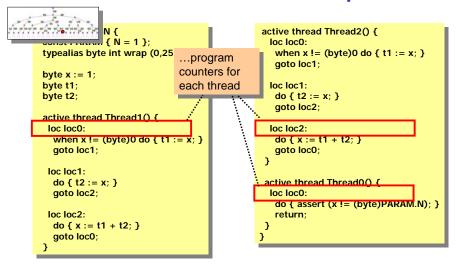
The state vector is the data structure corresponding to the state (as previously discussed). It holds the value of all variables as well as program counters for each process, and represents a particular position in the computation tree



35

33

SumToN State Vector Example



Example State Vector: [0,0,2,1,1,1]

CISC422/853, Winter 2009

Model Checking, Part 1

Source: 842@KSU

37

SumToN Assertion Violation

```
active thread Threadk() {
      loc loc0:
       when x != (byte)0 do {
       t1 := x; } goto loc1;
       loc loc1:
       do \{t2 := x; \}
       goto loc2;
       do \{ x := t1 + t2; \}
       goto loc0;
     active thread Thread0() {
      loc loc0:
0:0
       do {
         assert (x !=
          (byte)PARAM.N); }
       return;
```

Violating schedule for N = 2

CISC422/853, Winter 2009

Model Checking, Part 1

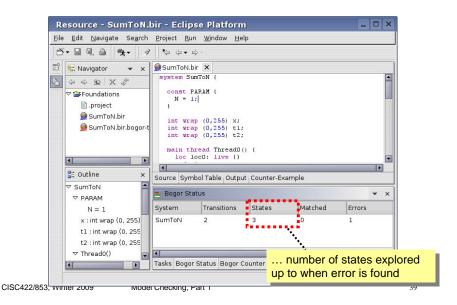
Source: 842@KSU

29

Depth-first Stack

Growt

Bogor Output

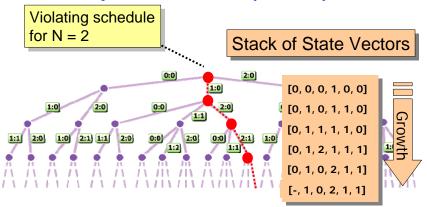


Depth-First Stack



- The depth-first stack serves two purposes:
 - When search comes to end of a path (or a state that has been seen before) and backtrack, the top of stack tells us where to backtrack to
 - If an error is encountered, the current contents of stack gives the computation path that leads to the error (counter example)

Depth-First Stack (Cont'd)



The depth-first stack can be implemented to hold

state vectors (straight-forward implementation)

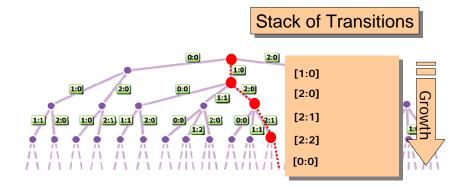
CISC422/853. Winter 2009

Model Checking, Part 1

Source: 842@KSU

41

Depth-First Stack (Cont'd)



The depth-first stack can be implemented to hold

transitions (requires less space, but harder to implement)

CISC422/853, Winter 2009

Model Checking, Part 1

Source: 842@KSU

42

Depth-first Stack of Transitions

 Generating a new state s_{new} requires analyzer to execute a transition t on current state s:

$$s_{new} = execute(t, s)$$

- Since analyzer is not holding states in the stack,
 - if it needs to back-track and return to a previously encountered state s_{prev}, it needs to be able to "undo" a transition t

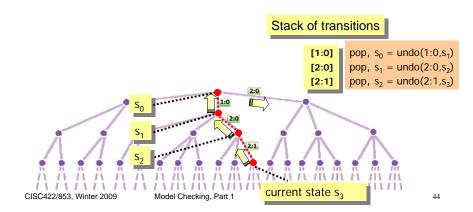
$$s_{prev} = undo(t, s)$$

 when providing variable values as diagnostic information for an error path, the analyzer needs a "simulation mode" where choice points are decided by the stacked transitions

Depth-first Stack of Transitions (Cont'd)

- Since analyzer is not holding states in the stack,
 - if it needs to back-track and return to a previously encountered state s_{prev}, it needs to be able "undo" a transition t

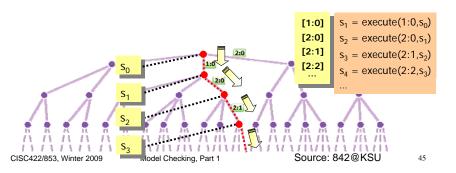
$$s_{prev} = undo(t, s)$$



Depth-first Stack of Transitions (Cont'd)

- Since analyzer is not holding states in the stack,
 - when providing variable values as diagnostic information for an error path, analyzer needs a simulation mode where choice points are decided by the stacked transitions

Stack of transitions leading to error state



Depth-First Stack of Transitions (Cont'd)

- Many model-checkers (including SPIN and Bogor) implement a depth-first stack of transitions
- This reduces amount of required memory and meshes well with its other space optimizations (e.g., bit-state hashing – discussed in Topic 8)

CISC422/853, Winter 2009

Model Checking, Part 1

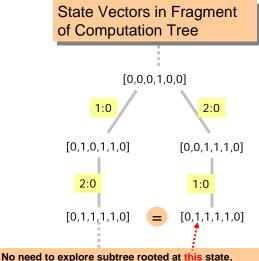
16

Seen State Set

- There may be more than one path to a given state
- If a state is reached for a second time, there is no need to check s again (or any of the children of s in the computation tree)
- Seen State Set:
 - used to avoid exploring/checking a part of the computation tree that is identical to a part that has already been explored before
 - in Bogor: implemented as hash table

Revisiting Via A Different Path

```
active thread Threadk() {
      loc loc0:
       when x != (byte)0 do {
       t1 := x; } goto loc1;
       loc loc1:
       do \{t2 := x; \}
       goto loc2;
       loc loc2:
      do \{ x := t1 + t2; \}
       goto loc0;
     active thread ThreadO() {
      loc loc0:
0:0
       do {
        assert (x !=
          (byte)PARAM.N); }
       return;
```



CISC422/853, Winter 2009 Model Checking, Part 1 47 CISC422/853, Winter 2009 Model Checking, Part 1

Computation Tree as Graph

```
active thread Threadk() {
       when x != (byte)0 do {
       t1 := x; } goto loc1;
      loc loc1:
       do \{t2 := x; \}
       goto loc2;
      loc loc2:
       do { x := t1 + t2; }
       goto loc0;
     active thread Thread0() {
      loc loc0:
0:0
       do {
        assert (x !=
          (byte)PARAM.N); }
       return;
```

```
Sometimes we view the computation tree as a graph

[0,0,0,1,0,0]

1:0

2:0

[0,1,0,1,1,0]

2:0

1:0

[0,1,1,1,1,0]

...sharing a node corresponds to (re)visiting a node that has been seen before.
```

CISC422/853, Winter 2009 Model Checking, Part 1

Seen State Set

```
active thread Threadk() {
                                            Computation Tree
                                                                          Seen Set
      loc loc0:
       when x != (byte)0 do {
       t1 := x; } goto loc1;
                                                 [0,0,0,1,0,0]
                                                                          [0,0,0,1,0,0]
                                                                          [0,1,0,1,1,0]
       loc loc1:
                                                                          [0,1,1,1,1,0]
       do \{t2 := x; \}
       goto loc2;
                                                                          [0,0,1,1,1,0]
                                                              2:0
                                             1:0
       loc loc2:
       do { x := t1 + t2; }
       goto loc0;
                                        [0,1,0,1,1,0]
                                                             [0,0,1,1,1,0]
     active thread ThreadO() {
      loc loc0:
0:0
       do {
         assert (x !=
                                        [0,1,1,1,1,0]
                                                             [0,1,1,1,1,0]
          (byte)PARAM.N); }
       return;
                                                         When Bogor gets to this state, it checks
                                       the Seen Set and finds it already has been checked, so it
                             Model Chec backtracks from this point
CISC422/853. Winter 2009
```

Non-Terminating Systems

- Let S be a finite state machine
 - Due to the use of the Seen Set, checking S will always eventually terminate
 - Even if S has non-terminating executions (Why?)
- Example: Consider the system on the right...
 - 1. Does execution of the system terminate?
 - 2. How many states does it have?
 - 3. Does an exhaustive analysis of the state-space of the system terminate?

```
boolean x;

active thread Thread1() {
  loc loc0: do { x := !x; }
  goto loc0;
}

active thread Thread2() {
  loc loc0: do { x := !x; }
  goto loc0;
}
```

Finite is not Enough

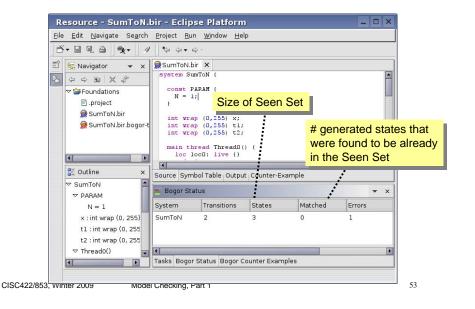
- So, the analysis of every BIR or PROMELA program will always terminate...
- ... but it may take a really long time to do so
- So, state spaces should not only be finite, but also "small enough" for the exploration to be feasible
- State Explosion Problem: Size of state space grows exponentially with the number of parallel processes
- Beware of systems with
 - large numbers of parallel processes
 - variables ranging over large domains (e.g., int, long)
 - variables ranging over large, complex data
- large numbers of variables
 CISC422/853, Winter 2009 Model Checking, Part 1

too many states; analysis takes too much time

states too large; analysis requires too much space₅₂

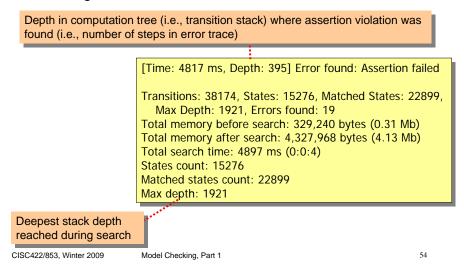
CISC422/853, Winter 2009 Model Checking, Part 1

Bogor Output

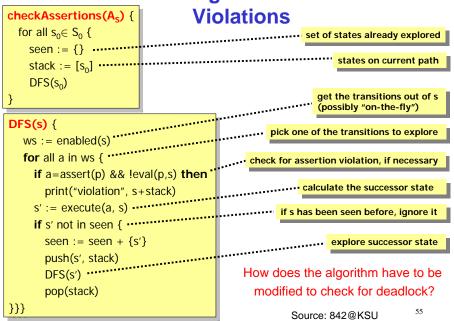


Bogor Output (Cont'd)

Running a model-check of SumToN with N = 5:



Checking for Assertion



Error Trace Length

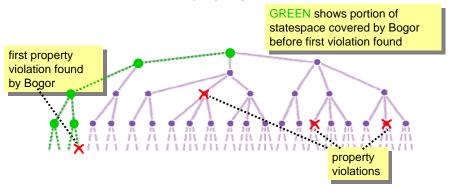
- Model-check SumToN with N = 5
- From Bogor's output, can see that execution trace that violates assertion was found and that trace is 395 steps long
 - Having to reason about how assertion can be violated along a trace of 395 steps is quite painful!
 - You have previously discovered a much shorter violating trace using Bogor's simulation mode.
 - Does this mean that the Bogor analyzer is not very useful?
 Not at all!!
- We will see now how to tell Bogor to search for shorter violating traces (as well as minimal length violating traces)

CISC422/853, Winter 2009

Model Checking, Part 1

Error Trace Length

 In general, a system may have many different traces that lead to the same property violation



 Because Bogor does a depth-first search (instead of a bread-first search), first violating trace found is usually not of minimal length

CISC422/853, Winter 2009

Model Checking, Part 1

Source: 842@KSU

--

Setting Bogor's Depth Bound

 Users can set a bound on the depth of Bogor's search (i.e., number of entries in Bogor's depth-first stack)

Key	Value			
edu.ksu.cis.projects.bogor.module.IActionTaker	edu.ksu.cis.projects.bogor.module.DefaultActionTaker			
edu.ksu.cis.projects.bogor.module.IBacktrackingInfoFactory	edu.ksu.cis.projects.bogor.module.backtrack.DefaultBacktrac			
edu.ksu.cis.projects.bogor.module.IExpEvaluator	edu.ksu.cis.projects.bogor.module.DefaultExpEvaluator			
edu.ksu.cis.projects.bogor.module.ISchedulingStrategist	edu.ksu.cis.projects.bogor.module.DefaultSchedulingStrategis			
edu.ksu icis.prejects ibogon module i Searcher	edurlisus cist projects, bogor module: Default Searcher			
edu.ksu.cis.projects.bogor.module.ISearcher.maxDepth	2000			
edutksu.cistprojects.bogor.module.ISearcher.maxErrors*****				
edu.ksu.cis.projects.bogor.module.IStateFactory	edu.ksu.cis.projects.bogor.module.state.DefaultStateFactory			
edu.ksu.cis.projects.bogor.module.IStateManager	edu.ksu.cis.projects.bogor.module.DefaultStateManager			
edu.ksu.cis.projects.bogor.module.ITransformer	edu.ksu.cis.projects.bogor.module.DefaultTransformer			
edu.ksu.cis.projects.bogor.module.IValueFactory	edu.ksu.cis.projects.bogor.module.value.DefaultValueFactory			

Choose the "Configure Bogor" option, then Add/Edit to set the value for the ISearcher.maxDepth attribute.

CISC422/853, Winter 2009

Model Checking, Part 1

50

Setting Bogor's Depth Bound

- This is often useful...
 - ...after a counterexample has been found and you want to see if a shorter one exists
 - look at Bogor's output to see the size, then rerun Bogor with an appropriate depth bound (i.e., one smaller than the size of the counter-example).
 - ...before a counterexample has been found and Bogor is taking too long or is running out of memory

Setting Bogor's Depth Bound (Cont'd)

- Be careful!
 - when search is bounded, Bogor will not explore parts of state space
 - unexplored part may contain property violations
 - If a bounded search does not find any violations, then
 - ° no violations in parts that got searched
 - ° but may have violations in unsearched parts
 - ⇒ A depth-bounded search may be incomplete!
- Bogor displays "Max depth reached!!!" whenever depth bound is reached and analysis may be incomplete

CISC422/853, Winter 2009 Model Checking, Part 1 59 CISC422/853, Winter 2009 Model Checking, Part 1 60

For Example

Checking SumToN with N = 5

System	Transitions	States	Matched	Max. Depth	Errors	Time	Status
SumToN	38174	15276	22899	1921	19	0:0:4	Done
SumToN	25	26	0	25	1	0:0:0	Done
SumToN	25	26	0	25	1	0:0:0	Done
SumToN	123	63	61	6	0	0:0:0	Done
SumToN	123	63	61	6	0	0:0:0	Done
SumToN	39	23	17	3	0	0:0:0	Done
SumToN	39	23	17	3	0	0:0:0	Done
SumToN	463	225	239	12	1	0:0:0	Done
Sumfol	382	189	194	11		0:6:0	Done
SumToN	296	155	142	10	1	0:0:0	Done
SumToN	275	134	142	9	0	0:0:0	Done

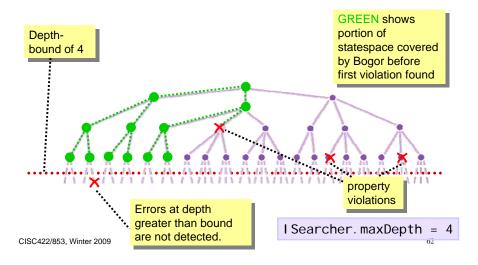
CISC422/853, Winter 2009

Model Checking, Part 1

61

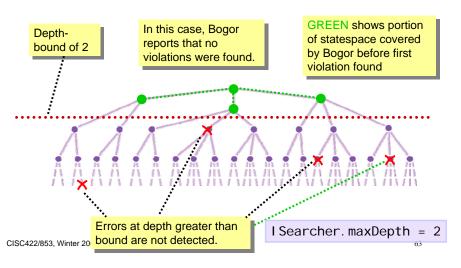
Bounded Depth-first Search

When analyzing a system and given a depth bound as a commandline argument, Bogor will backtrack when that depth is reached



Bounded Depth-first Search (Cont'd)

When analyzing a system and given a depth bound as a commandline argument, Bogor will backtrack when that depth is reached



Depth-Bounded DFS

Advantages: ?

Disadvantages: ?

CISC422/853, Winter 2009 Model Checking, Part 1

Finding the Shortest Counter Example

- Using Bounded DFS
 - in Bogor:
 - ° start with high bound that finds error
 - $^{\circ}\,$ successively lower the bound until no error
 - in Spin:
 - ° Run verifier with option –i or –l:

```
pan.exe -i Or pan.exe -l
```

Using?

Yes, Breadth First Search!

- How to make Bogor and Spin use BFS
 - in Bogor:
 - ° write routine and plug it in
 - ° modular architecture of Bogor makes this easy
 - in Spin:
 - $^{\circ}\,$ compile verifier with –DBFS option:

- Easy to implement
- What're the advantages of BFS over DFS?
- What're the disadvantages?

CISC422/853, Winter 2009 Model Checking, Part 1 65 CISC422/853, Winter 2009 Model Checking, Part 1 65